# **3-GHz Frequency Divider**

#### Description

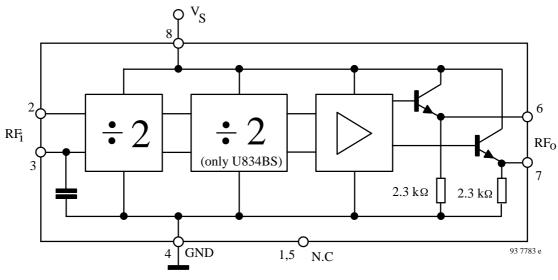
U832BS and U834BS use TEMIC's advanced bipolar process. RF input can be driven differential as well as

- U832BS divides by 2 •
- U834BS divides by 4
- Very low current consumption (typically 12 mA)
- 3-GHz maximum operating frequency
- Supply voltage, typically 5 V
- ESD protection in accordance with MIL-STD. 883 • method 3015 class 2

single ended. Low current consumption makes the device suitable for mobile application.

#### **Benefits**

- Extended operation time due to very low current • consumption
- Only three external components •
- Low RF input level reduces radiation problems •



**Block Diagram** 

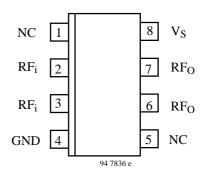
Figure 1. Block diagram

#### **Ordering Information**

Extended Type Number	Package	Remarks
U832BS-FP	SO8	
U834BS-FP	SO8	

# U832BS / U834BS

Pin	Symbol	Function
1	NC	Not connected
2	RFi	RF input
3	RFi	RF input with internal decoupling capacitor
4	GND	Ground
5	NC	Not connected
6	RFO	Output
7	RFO	Output
8	Vs	Supply voltage



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# **Absolute Maximum Ratings**

Parameters	Symbol	Value	Unit
Supply voltage Pin 8	Vs	6	V
Input voltage range Pins 2 and 3	Vi	0 to V <sub>S</sub>	V
Junction temperature	Тj	125	°C
Storage temperature range	T <sub>stg</sub>	-40 to $+125$	°C

### **Operating Range**

Parameters	Symbol	Value	Unit
Supply voltage range	Vs	4.5 to 5.5	V
Ambient temperature range	T <sub>amb</sub>	- 25 to + 85	°C

### **Thermal Resistance**

Parameters	Symbol	Value	Unit
Junction ambient SO8	R <sub>thJA</sub>	175	K/W



## **Electrical Characteristics**

$V_S = 4.5$ to 5.5 V, $T_{amb} = 0$ to + 70°C, referred to test circuit, unless otherw	ise specified.
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Parameters	Test Conditions / Pin	Symbol	Min	Тур	Max	Unit
Supply current	$V_S = 5 V$ Pin 8	IS				
	U832BS (: 2)	~		12	16	mA
	U834BS (: 4)			13	17.5	mA
RF Input	Pin 2	RFi				
Input sensitivity <sup>1)</sup>	$R_S = 50 \ \Omega$	V <sub>iRF</sub>	80		300	mV
Input frequency range	$R_S = 50 \Omega$ Pin 2	f <sub>imin</sub>			300	MHz
		f <sub>imax</sub>	3000			
RF output	Pins 6 and 7	RFO				
Output level	$V_S = 5 V, R_6, R_7 = \infty$	V <sub>RFo</sub>		-8		
-	$R_L = 50 \Omega, R_6, R_7 = 1 K$			-5		dBm
DC output level	$R_{L} \geq 10 k\Omega$					
_	$V_{S} = 5 V$					
	High	<b>RF</b> DCH		3.6		V
	Low	RF <sub>DCL</sub>		3.1		V

<sup>1)</sup> RMS-voltage calculated from the measured available power.  $R_S = System$  resistance,  $R_L = Load$  resistance

### **Output Stage**

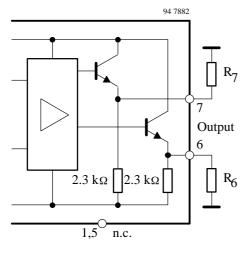


Figure 2.

### Input Sensitivity vs. Frequency

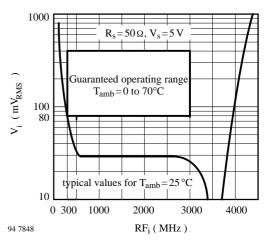


Figure 3.

# U832BS / U834BS



# **Input Impedance S11**

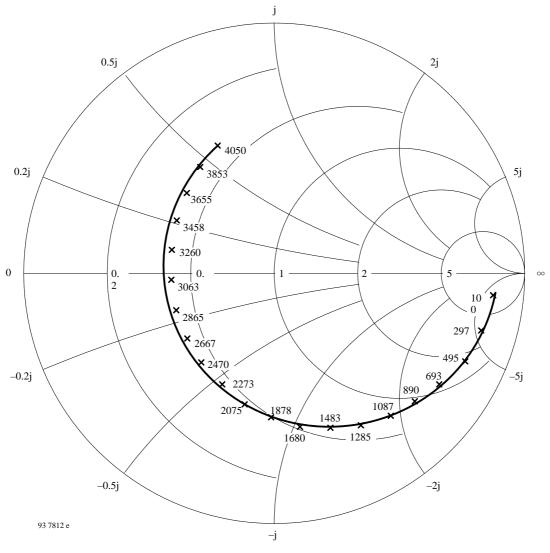


Figure 4.



#### Application

Master-Slave-D-Flip-Flops (MS-D-FF's) can be used for frequency division by feeding back the inverted output to the data input. Typical for this kind of dividers is a free– running oscillation of the first divider stage. Here, the input sensitivity of the circuit is minimal (see data sheet page 5: input sensitivity vs. frequency). An oscillation frequency of, e.g., 3.5 GHz (related to the input) will result in an output the part number of frequency of 3.5 GHz: 4 = 875 MHz. To indicate this, TEMIC's selfoscillating frequency dividers are ending with "S" (U834BS-AFP). This oscillation is often used for the quality control of ICs: the higher the frequency, the better the performance. It occurs only if the offset voltage of the input transistor pair is below a certain limit.

On the other hand, if this oscillation causes problems, e.g., in frequency counters, an external offset may be added. This can simply be done by connecting an ohmic resistor from Pin 3 (RF input) to ground. Using a value of  $R_3 = 47 \text{ k}\Omega$  will stop self oscillation without degrading the input sensitivity markly. Smaller values will decrease the sensitivity especially at higher frequencies.

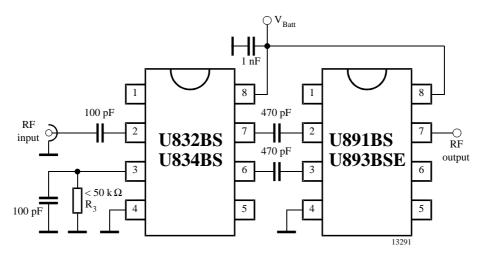
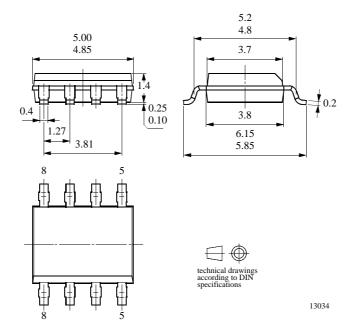


Figure 5.

#### **Package Information**

Package SO8 Dimensions in mm



# U832BS / U834BS

### **Ozone Depleting Substances Policy Statement**

#### It is the policy of **TEMIC TELEFUNKEN microelectronic GmbH** to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

**TEMIC TELEFUNKEN microelectronic GmbH** semiconductor division has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

**TEMIC** can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice. Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use TEMIC products for any unintended or unauthorized application, the buyer shall indemnify TEMIC against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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